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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Robert Bristol et al.	§	Group Art Unit:	1756
		§		
Serial No.:	10/679,816	§	Examiner:	Kathleen Duda
		§		
Filed:	October 6, 2003	§	Atty. Dkt. No.:	ITL.1023US
		§		(P16710)
For:	Enhancing Photoresist	§		
	Performance Using Electric Field	§	Assignee:	Intel Corporation

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant submits the references listed on the attached form PTO 1449, copies of which are enclosed. A copy of a communication dated August 29, 2007 from a foreign patent office in a counterpart application is also enclosed. I, the undersigned, hereby certify that each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement. Please apply any charges or credits to Deposit Account No. 20-1504 (ITL.1023US).

Respectfully submitted,

September 20, 2007

Date

Timothy N. Trop, Reg. No. 28,994
Trop, Pruner & Hu, P.C.
1616 S. Voss Rd., Ste. 750
Houston, TX 77057-2631
713/468-8880
713/468-8883 [fax]

Attorney for Intel Corporation

Customer No.

21906

Date of Deposit: September 20, 2007

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Monica Jacobs